



**ARKA JAIN
University**
Jharkhand



Circular

Ref. No. AJU/AD/ENGG/162/2023-24

Date: 07/03/2024

This is to notify all students and faculty members that department of Electrical and Electronics Engineering, School of Engineering and IT, ARKA JAIN University commences a Short-Term Value-Added Course on "**Design and Methodologies of Multilevel Inverter Topologies**" from **12th March 2024** to **31st March 2024**. We strongly encourage all students and faculty members who are interested to join and take part in this initiative. As such, we kindly request that all interested individuals register for participation by using the provided link below.

Link for registration: <https://forms.gle/kmvr8jk6RQ9CKWTF9>

- **Last Date for Registration: 11.03.2024, up to 11:59 p.m.**
- **Mode: Hybrid**

Convener:

Dr. Ashwini Kumar (dr.ashwini@arkajainuniversity.ac.)

Coordinators:

Dr. Kasinath Jena (dr.kasinath@arkajainuniversity.ac.in)
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