

Membrane-Dependent Neuromorphic Learning Rule for Unsupervised Spike Pattern Detection

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Abstract—Several learning rules for synaptic plasticity, that depend on either spike timing or internal state variables, have been proposed in the past imparting varying computational capabilities to Spiking Neural Networks (SNNs). Due to design complications these learning rules are typically not implemented on neuromorphic devices leaving the devices to be only capable of inference. In this work we propose a unidirectional post-synaptic potential dependent learning rule that is only triggered by pre-synaptic spikes, and easy to implement on hardware. We demonstrate that such a learning rule is functionally capable of replicating computational capabilities of pairwise STDP. Furthermore, we demonstrate that this learning rule can be used to learn and classify spatio-temporal spike patterns in an unsupervised manner using individual neurons. We argue that this learning rule is computationally powerful and also ideal for hardware implementations due to its unidirectional memory access.

I. INTRODUCTION

Neuromorphic devices aim to capitalize on event based temporal processing and unsupervised learning features of brain inspired spiking neural networks, for efficient, low-power and robust computation. Learning capability in neuromorphic devices is obtained through the use of plastic synapses. Several neuromorphic systems are being developed with the capability to learn synaptic weights of spiking neurons implemented on silicon [1].

One of the major blocks constituting neuromorphic devices with plastic synapses is the memory required to store synaptic weights in addition to the circuitry required to implement the learning rule. While some neuromorphic devices have dedicated plastic synaptic circuits [2], [3] or have a cross bar type synaptic memory [4], in most neuromorphic devices, synaptic connectivity and weight memory is implemented on external or internal dedicated digital RAM. Implementation of dedicated plastic synapse circuit for every individual synapse dramatically increases the area requirement for neuromorphic chips, which is not desirable.

A second factor that discourages implementation of plasticity on neuromorphic devices is the added complexity in the type of memory required to implement current spike based learning rules. Pairwise Spike-Timing Dependent Plasticity (STDP) [5] is a widely used learning rule in computational models of spiking neural networks [6], [7], [8]. Event driven implementation of this learning rule triggers an update in the synaptic weight both at the arrival of a presynaptic spike and

generation of a postsynaptic spike. This necessitates the accesses to memory associated with the corresponding synaptic weight location in a bidirectional manner i.e. both based on the identity both pre-synaptic and postsynaptic neurons.

These factors hold true also for triplet-based STDP rule [9]. A physical implementation of such synaptic weight memory therefore has to meet these requirements in order to achieve efficient STDP. Traditional RAM devices are not designed for such access in an efficient manner, especially for sparsely connected networks. Instead one requires design of custom memory architecture such as the bidirectional crossbar memory architecture [4]. While this is a viable solution for the implementation of STDP based learning rules, it is very expensive in terms of total memory and silicon area.

Alternate learning rules that rely on the presynaptic spike timing and neuronal state variables are being explored for neuromorphic implementations [10]. These learning rules also tend to be biophysically plausible compared to the pairwise STDP. The authors of [11] present a model of spike-driven synaptic plasticity for bistable synapses where synapses are modified on the arrival of presynaptic spikes based on the postsynaptic potential and calcium concentration. They show that such a rule can enable a neuron to learn to classify complex stimuli in a semi-supervised fashion based on the input firing rates. A calcium voltage dependent plasticity model [12] has been shown to approximate several STDP curves experimentally observed in biology. Similarly, a Membrane Potential Dependent Plasticity (MPDP) rule proposed recently [13] has been shown to approximate anti-hebbian STDP like learning rule which has been argued to be in agreement with STDP of inhibitory synapses [14]. The authors show that this model can be used to train a neuron in a supervised manner to spike at precise times.

In this paper, we propose a simple post-synaptic membrane potential dependent event-driven learning rule that statistically emulates pairwise STDP like behavior for excitatory synapses. This learning rule is extremely hardware efficient due to its dependence only on the presynaptic spike timing and not on the postsynaptic spike timing. In addition we demonstrate that such a learning rule enables a neuron to learn and identify complex temporal spike patterns embedded in a stream of spikes in an unsupervised manner.

II. MATERIALS AND METHODS

A conductance based integrate and fire neuron model has been used to obtain results presented in this paper. The neuron dynamics are described by Eqns. 1 and 2.

$$C_m dV/dt = (V_{rest} - V)g_l + (E_s - V)g_e \quad (1)$$

$$\tau_s dg_e/dt = -g_e + \sum_i W_i S(t - t_i) \quad (2)$$

where V is the neuron's membrane potential, C_m is the membrane capacitance, V_{rest} is its resting potential, E_s is the synaptic reversal potential, g_l is the leakage conductance, g_e is the excitatory synaptic conductance, S are the presynaptic spike trains, W is the synaptic weight and τ_s is the synaptic time constant. In addition to the neuronal dynamics, each neuron has a calcium concentration trace associated to it. Calcium dynamics is given by Eqn. 3

$$\tau_{ca} dCa/dt = -Ca + S(t - t_{post}) \quad (3)$$

where Ca is the calcium concentration, τ_{ca} is the decay time constant and t_{post} is spike timing of post synaptic neuron. The calcium concentration effectively follows a low pass filtered version of neuron's spiking activity.

A. Learning rule

Each neuron receives inputs from several presynaptic neurons with random initial weights. Over the course of input spike presentation the synaptic weights get updated. The weight update rule consists of two components ΔW_v and ΔW_h .

$$\Delta W = \Delta W_v + \Delta W_h \quad (4)$$

The weight update component based on the post-synaptic potential ΔW_v is given by:

$$\Delta W_v = [\delta(V_m(t+1) > V_{lth})\eta_+ - \delta(V_m(t+1) < V_{lth})\eta_-]S(t - t_{pre}) \quad (5)$$

where $\delta(True) = 1$ and $\delta(False) = 0$. η_+ and η_- are the magnitude of positive and negative weight updates. V_{lth} is the membrane threshold voltage that determines whether the weight should be potentiated (LTP) or depressed (LTD). S represents a pre-synaptic spike train.

Homeostatic weight update component ΔW_h is given by:

$$\Delta W_h = \eta_h (Ca_t - Ca)S(t - t_{pre}) \quad (6)$$

where Ca_t is the target calcium concentration, Ca is the current calcium concentration and η_h is the magnitude of rate of homeostasis.

B. Stability conditions for the learning rule

In order to ensure that the weights learned are reflective of the statistics of the inputs and do not drift to maximum or minimum bounds together, the parameters of the learning rule need to be appropriately chosen. We postulate the following three criteria: *i)* For a random spike train at a synapse, with no correlation to the input spike pattern, the weights should drift

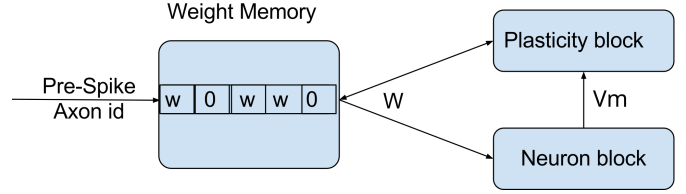


Fig. 1. A block diagram of data flow in conjunction with neuromorphic hardware using an index/pointer based weight memory.

towards 0. This is described by the Eqn. 7. *ii)* The negative weight updates triggered by spikes uncorrelated with the target spike pattern (when the pattern is not being presented) should not be large enough to nullify the potentiation during the presentation of the pattern. This is described by the Eqn. 8. *iii)* The homeostasis should be strong enough to drive the weights up when the firing rate is too low. This is ensured by Eqn. 9.

$$t_p \eta_+ \leq t_n \eta_- \quad (7)$$

$$\eta_+ > t_n f \eta_- \quad (8)$$

$$\eta_- \leq C a_t \eta_h \quad (9)$$

where t_p is the total duration of the spike pattern, t_n is the average duration of noise or uncorrelated spike patterns presented between patterns.

C. Hardware implementation

As previously discussed, synaptic memory constitutes a large fraction of neuromorphic device area, and therefore it is desirable to use techniques that allow compression of this synaptic memory. Compressed memory schemes such as index based or linked list based routing and weight storage schemes are ideal if the connectivity of the network is sparse. The key limitation of such storage schemes is that of unidirectional memory access ie. if we store connectivity based on source addresses, we can only determine all destinations from a given source (in $O(1)$) but not all sources given destination. This limits the implementation of learning rules that trigger a weight update by both pre- and post synaptic neurons. The learning rule we propose here can take advantage of such compressed memory schemes and still perform synaptic plasticity. Fig. 1 shows a block diagram of the use of a unidirectional pointer based weight table with the proposed learning rule.

III. RESULTS

In order to evaluate the proposed learning rule we conduct a series of experiments described in the following section. Input and output sizes are arbitrarily chosen and verified to work with different sizes (not shown).

A. Equivalence to STDP

The first experiment consists of a statistical measure of the weight updates triggered by the arrival of a presynaptic spike with respect to postsynaptic spike time. A single post synaptic neuron was presented with random Poisson spike patterns across 225 synapses. While freezing all the weights,

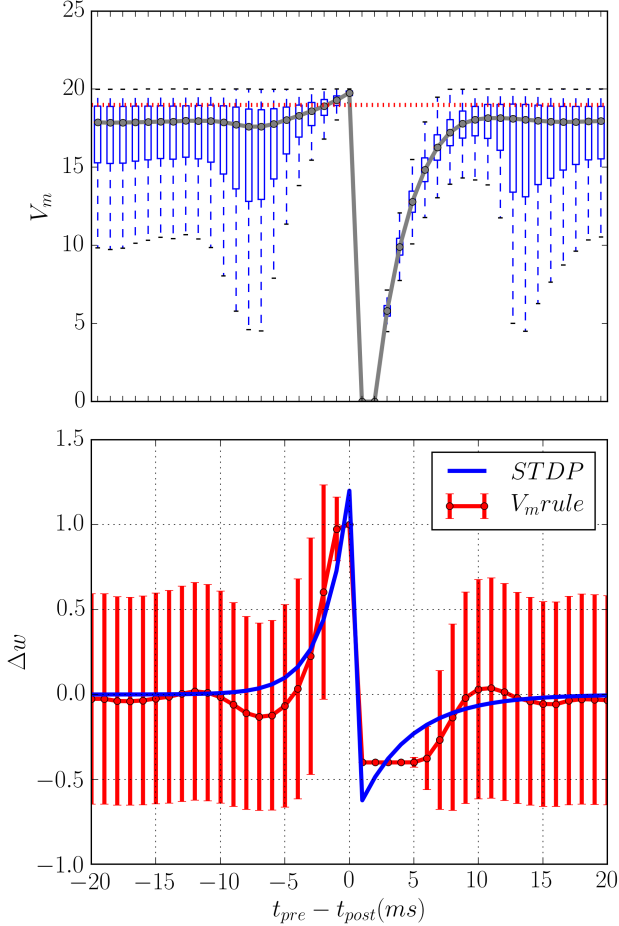


Fig. 2. (Top) The membrane potential distribution before and after a post-synaptic spike when activated using a stream of Poisson spike trains. The dotted horizontal line in red shows the learning threshold. (Bottom) The mean and standard deviation of weight update before and after a post-synaptic spike as a result of the V_m based learning rule proposed in this paper in red. An empirically equivalent curve of exponential STDP rule is plotted in blue.

the hypothetical weight updates governed by the learning rule were recorded over time. The recorded data was sorted w.r.t postsynaptic spike timing. The membrane potential w.r.t the spike timing of the postsynaptic neurons was also recorded. The top plot of Fig. 2 shows the distribution of membrane potential approaching spiking threshold just before a postsynaptic spike, and is close to resting potential right after. The red curve on the bottom plot shows positive weight updates (LTP) when a presynaptic spike arrives just before the postsynaptic neuron fires and negative (LTD) when a presynaptic spike arrives just after the postsynaptic neuron fires. Beyond a certain time window, the mean weight update is zero, although individual updates have a large variance.

As can be seen in Fig. 2, the membrane based learning rule proposed here empirically approximates the pairwise exponential STDP rule.

B. Coincidence detection

A fundamental computational capability of STDP empowered neurons is believed to be able to learn correlated activity

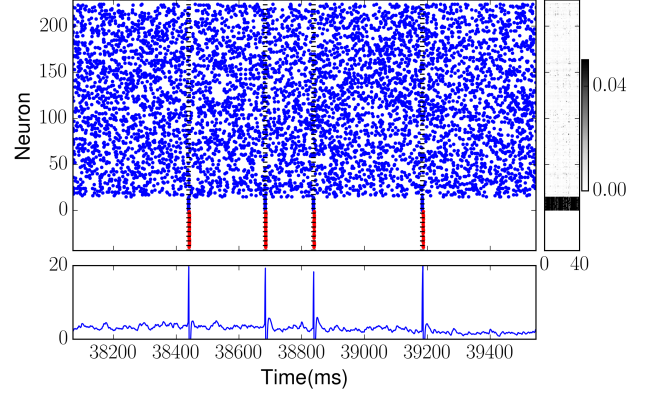


Fig. 3. Raster plot of spikes from 225 inputs to 40 neurons (in blue). The first 20 of inputs are synchronous. The synaptic learning rule drives the weights of synchronously firing inputs to potentiate and other synaptic weights are depressed as can be seen in the grey scale image of the right sub-plot. As a result the post synaptic neurons only fire (in red) when there is coincidence of spikes. An example membrane potential of one of the 40 neurons is shown in the lower sub plot.

in a form of Hebbian learning. To verify this ability with the proposed learning rule we setup the following experiment. 40 neuron receives inputs from 225 spike sources over plastic synapses. 20 of the inputs are generated from a single Poisson process with mean firing frequency of 5 Hz and consequently the spikes from these inputs are always synchronous. The remaining input are generated from independent Poisson processes with mean spiking frequency of 20 Hz (See Fig. 3), and therefore lack synchronicity.

The weights of all 225 synapses are initialized randomly from a uniform distribution. Over time the weight of synapses with synchronous inputs, *ie.* coincident spiking activity drift to high weights. The input weights of synapses that lack synchrony are driven to low weights. As a result, the post synaptic neurons learn to fire only when there is coincident spiking activity from the first 20 inputs.

C. Hidden pattern detection

In the above subsection we presented inputs where some of the input sources always firing synchronously. We saw that under these circumstances the learning rule is able to potentiate the corresponding synapses and depress all other synapses. We now explore a different scenario when a randomly generated fixed spike pattern ‘SP’ is repeatedly presented, interspersed with random spike patterns of the same firing rate. Fig. 4 shows the presentation of such a stream of spikes to 20 different neurons over 225 synapses each. The initial weights were randomly initialized from a uniform distribution. Over time the synaptic weights converge such that the postsynaptic neurons selectively spike only on presentation of SP. It should be noted that not all neurons spike at the exact same time but fire in the neighborhood of SP, as can also be explained from the different final weights.

IV. DISCUSSION AND CONCLUSION

We propose a simple membrane based learning rule that enables individual neurons to learn and detect spatio-temporal spike patterns. In contrast to other works that shown that membrane based learning rules can be used to perform supervised learning, here we demonstrate that such a rule can imbue unsupervised temporal learning capabilities to neurons. We argue that, by only triggering weight updates from the pre-synaptic neuron, this learning rule is more amenable to hardware implementations where the weight is store using index based memory structures. This allows design of learning neuromorphic devices with minimal memory resources.

While this paper considers a single threshold on the membrane for the learning rule, future work is geared towards complex learning kernels on the membrane potential to mimic other STDP kernels [10] and optimize the learning process.

REFERENCES

- [1] C. G. Mayr, S. Sheik, C. Bartolozzi, and E. Chicca, "Research topic:synaptic plasticity for neuromorphic systems," *Frontiers in Neuroscience*, vol. 10, no. 214, 2016.
- [2] S. Mitra, S. Fusi, and G. Indiveri, "A vlsi spike-driven dynamic synapse which learns only when necessary," in *Circuits and Systems, 2006. ISCAS 2006. Proceedings. 2006 IEEE International Symposium on*, pp. 4–pp, IEEE, 2006.
- [3] N. Qiao, H. Mostafa, F. Corradi, M. Osswald, F. Stefanini, D. Sumislawska, and G. Indiveri, "A reconfigurable on-line learning spiking neuromorphic processor comprising 256 neurons and 128k synapses," *Frontiers in neuroscience*, vol. 9, 2015.
- [4] J.-s. Seo *et al.*, "A 45nm cmos neuromorphic chip with a scalable architecture for learning in networks of spiking neurons," in *Custom Integrated Circuits Conference (CICC)*, IEEE, 2011.
- [5] G. Q. Bi and M. M. Poo, "Synaptic modifications in cultured hippocampal neurons: dependence on spike timing, synaptic strength, and postsynaptic cell type," *The Journal of neuroscience*, vol. 18, no. 24, pp. 10464–10472, 1998.
- [6] T. Masquelier, R. Guyonneau, and S. J. Thorpe, "Spike timing dependent plasticity finds the start of repeating patterns in continuous spike trains," *PLoS one*, vol. 3, no. 1, p. e1377, 2008.
- [7] B. Nessler, M. Pfeiffer, and W. Maass, "Stdp enables spiking neurons to detect hidden causes of their inputs," in *Advances in neural information processing systems*, pp. 1357–1365, 2009.
- [8] E. Neftci, S. Das, B. Pedroni, K. Kreutz-Delgado, and G. Cauwenberghs, "Event-driven contrastive divergence for spiking neuromorphic systems," *Frontiers in Neuroscience*, vol. 7, no. 272, 2014.
- [9] J.-P. Pfister and W. Gerstner, "Triplets of spikes in a model of spike timing-dependent plasticity," *The Journal of neuroscience*, vol. 26, no. 38, pp. 9673–9682, 2006.
- [10] D. E. Feldman, "The spike-timing dependence of plasticity," *Neuron*, vol. 75, no. 4, pp. 556–571, 2012.
- [11] J. M. Brader, W. Senn, and S. Fusi, "Learning real-world stimuli in a neural network with spike-driven synaptic dynamics," *Neural computation*, vol. 19, no. 11, pp. 2881–2912, 2007.
- [12] M. Graupner and N. Brunel, "Calcium-based plasticity model explains sensitivity of synaptic changes to spike pattern, rate, and dendritic location," *Proceedings of the National Academy of Sciences*, vol. 109, no. 10, pp. 3991–3996, 2012.
- [13] C. Albers, M. Westkott, and K. Pawelzik, "Learning of precise spike times with homeostatic membrane potential dependent synaptic plasticity," *PLoS one*, vol. 11, no. 2, p. e0148948, 2016.
- [14] J. S. Haas, T. Nowotny, and H. D. Abarbanel, "Spike-timing-dependent plasticity of inhibitory synapses in the entorhinal cortex," *Journal of neurophysiology*, vol. 96, no. 6, pp. 3305–3313, 2006.
- [15] T. Masquelier, R. Guyonneau, and S. J. Thorpe, "Competitive stdp-based spike pattern learning," *Neural computation*, vol. 21, no. 5, pp. 1259–1276, 2009.
- [16] S. Habenschuss, J. Bill, and B. Nessler, "Homeostatic plasticity in bayesian spiking networks as expectation maximization with posterior constraints," in *Advances in Neural Information Processing Systems*, pp. 773–781, 2012.

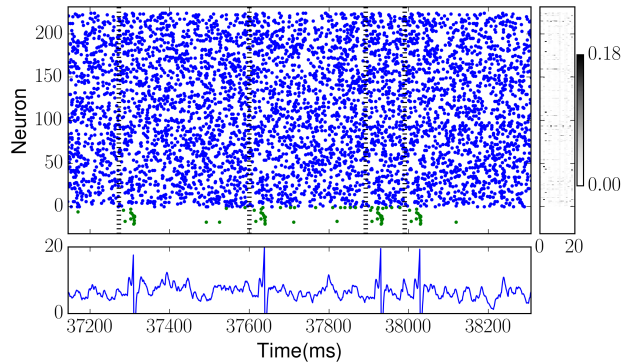


Fig. 4. Neurons receive Poisson spike trains from 225 pre-synaptic neurons. A 40 ms spike pattern (SP) is repeatedly presented with a mean frequency of 5 Hz (onset marked by vertical dotted lines) interspersed with random Poisson spikes of varying lengths. Both the spike pattern (SP) and random spikes have a mean spiking frequency of 20 Hz. After training with this input, the output neurons' response synchronizes with the presentation of SP as can be seen in the lower subplot. The final weights are shown in the right sub-plot.

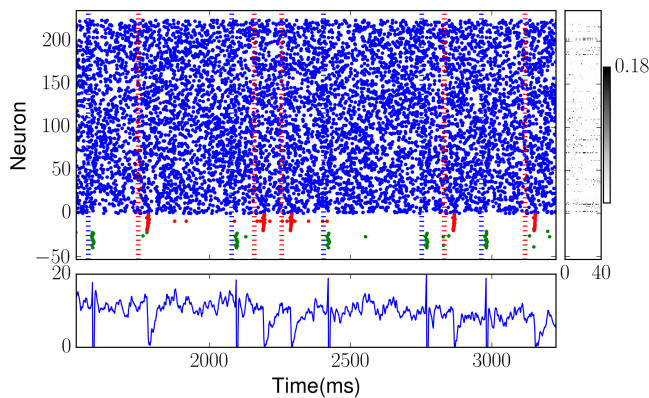


Fig. 5. Raster plot of input (blue) and output spike trains of two competing populations of neurons (red and green) learn to identify two different spike patterns (onsets marked with vertical dotted lines) embedded in noisy spike train.

D. Multiple pattern detection

The results above demonstrate that single patterns embedded in noise are detected and learned based on the learning rule. We now explore a more practical scenario where multiple patterns with statistical significance are presented to the neurons. More specifically, here we present two different patterns in random order interspersed with noise similar to the single pattern presentation experiment. In order to ensure each of the coincidence detectors are tuned to one of the patterns of interest, a competitive winner-take-all is imposed on the coincidence detector neurons [15], [16]. Two populations of 20 neurons each with self-excitatory connections and mutual inhibitory connections constitute this winner-take-all network of coincidence detectors. As shown in Fig. 5 the two populations indeed self organize to detect one of the two spike patterns. The competition allows the populations to converge onto different spike patterns.